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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/745,223	12/21/2000	Mark Francis Rumreich	PU000182	7578

7590 05/08/2003
Joseph S. Tripoli
THOMSON multimedia Licensing Inc.
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Princeton, NJ 08543

EXAMINER

YENKE, BRIAN P

ART UNIT	PAPER NUMBER
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2614

DATE MAILED: 05/08/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary	Application No.		Applicant(s)	
	09/745,223		RUMREICH ET AL.	
	Examiner		Art Unit	
	BRIAN P. YENKE		2614	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on _____.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 4-7 is/are allowed.
- 6) ☒ Claim(s) 1 and 2 is/are rejected.
- 7) ☒ Claim(s) 3 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) <u>4</u> . | 6) <input type="checkbox"/> Other: |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-2 is rejected under 35 U.S.C. 102(b) as being anticipated by Fling et al., US 4,602,276.

In considering claim 1,

a) *the claimed an amplifier having an input for receiving a chroma input signal, an output for providing a chroma output signal* is met by saturation multiplier 34 (amplifier) which receives a chroma input signal from ACC Color Kill Ckt 28 (Fig 1) and saturation multiplier 34 outputs a chroma output signal to color demod 32 (col 3, 14-25).

b) *the claimed a control circuit coupled to the amplifier for controlling the amplitude of a burst component of the output signal characterized in that* is met by ACC color kill circuit 28 along with chroma overload detector (36) and control unit 38, where ACC color kill circuit 28 adjusts the amplitude of the chrominance signal to maintain the amplitude of the amplitude of the color burst constant (col 3, line 5-14), and where chroma overload detector (36) generates an average value of the chrominance signal and control unit 38 readjusts the saturation scale factor applied to multiplier 34 to hold the average image color saturation to users setting (col 3, line 34-42).

c) the claimed the control circuit reduces the amplitude of the output signal in a controlled manner when the value of the burst component is below a predetermined value is met where if the amplitude of the chrominance signal falls below a predetermined acceptable level, circuit 28 outputs a zero valued chrominance signal, which is received by multiplier 34, which is used by chroma overload detector (36) and control unit 38 to adjust the amplitude of the chrominance signal.

In considering claim 2,

The claimed characterized in that the control circuit comprises a first feedback path coupled to the amplifier and a second feedback path coupled to the first feedback path is met where control unit 38 includes a first feedback path to multiplier 34 (Fig 2) and chroma detector 36 includes a second feedback path to control 38 which receive the chroma output from multiplier 34, where chroma detector 36 is connected to control unit 38, thereby having a second feedback path connected to a first feedback path.

Allowable Subject Matter

2. Claims 4-7 are allowed.

Claim 3 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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The following is a statement of reasons for the indication of allowable subject matter:

Regarding claim 3, Prior art does not show a first feedback path comprising a filter where a second feedback path is connected in parallel with the filter for controlling the values of a knee (slope) for burst amplitudes below a given knee value.

Regarding claim 4, Prior art does not include all limitations including: a circuit for controlling the gain of the amplifier, where the circuit comprises a first feedback path including a cascade connection of a means for providing a signal representative of a measure burst amplitude, a means for providing an error signal representative of the difference between a desired burst amplitude and the measured burst amplitude, a second feedback path coupled from an output of the integrator to an input of the integrator for reducing the gain of the amplifier for burst component values below a threshold value.

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian Yenke whose telephone number is (703) 305-9871. The examiner work schedule is Monday-Thursday, 0730-1830 hrs.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's Supervisor, John W. Miller, can be reached at (703)305-4795.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

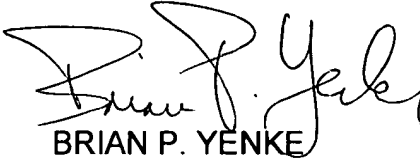
Washington, D.C. 20231

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or faxed to:

(703) 872-9314

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist). Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703)305-4700.


BRIAN P. YENKE
Patent Examiner
Art Unit 2614



B.P.Y.

May 5, 2003